

## DECLARATION FOR PATENT APPLICATION

As a below named inventor, I declare:  
 that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original, first and joint inventor (if more than one individual inventor is listed below) of the invention in

## SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE OF FAST RANDOM CYCLE SYSTEM AND TEST METHOD THEREOF

the specification of which is attached hereto unless the following box is checked.

was filed on \_\_\_\_\_ as United States Application  
 or PCT International Application No. \_\_\_\_\_ and  
 was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

Country	Category	Application No.	Filing Date	Priority Claim
Japan	Patent	2002-287868	September 30, 2002	Yes

I hereby appoint the registrants of Hogan & Hartson L.L.P.,  
 Biltmore Tower, 500 South Grand Avenue, Suite 1900, Los Angeles,  
 California 90071, Customer No. 26021, or any one of them.  
 Send correspondence to Hogan & Hartson L.L.P., Biltmore Tower, 500  
 South Grand Avenue, Suite 1900, Los Angeles, California 90071.  
 Please direct telephone calls to Customer No. 26021 at 213-337-6700.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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I declare further that my mailing address is at c/o  
Intellectual Property Division, Toshiba Corporation, 1-1, Shibaura  
1-chome, Minato-ku, Tokyo 105-8001, Japan; and  
that my citizenship and residence are as stated below next to my name:

Inventor: (Signature)

Date

Residence

Date: SEP. - 8.2003

*Kazuko Inuzuka*

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Citizen of: Japan

Yokohama-shi, Japan

Date: SEP. - 8.2003

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